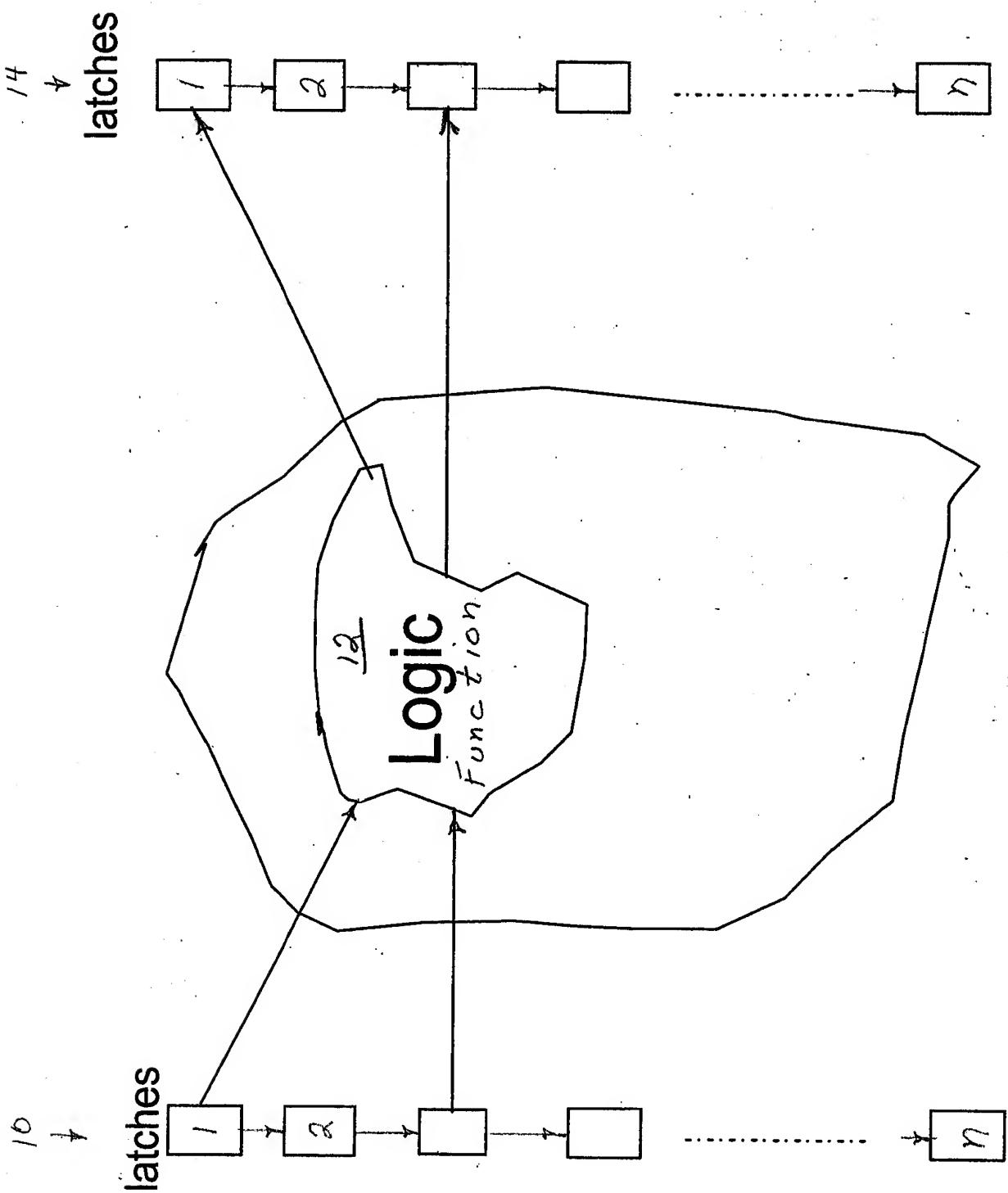


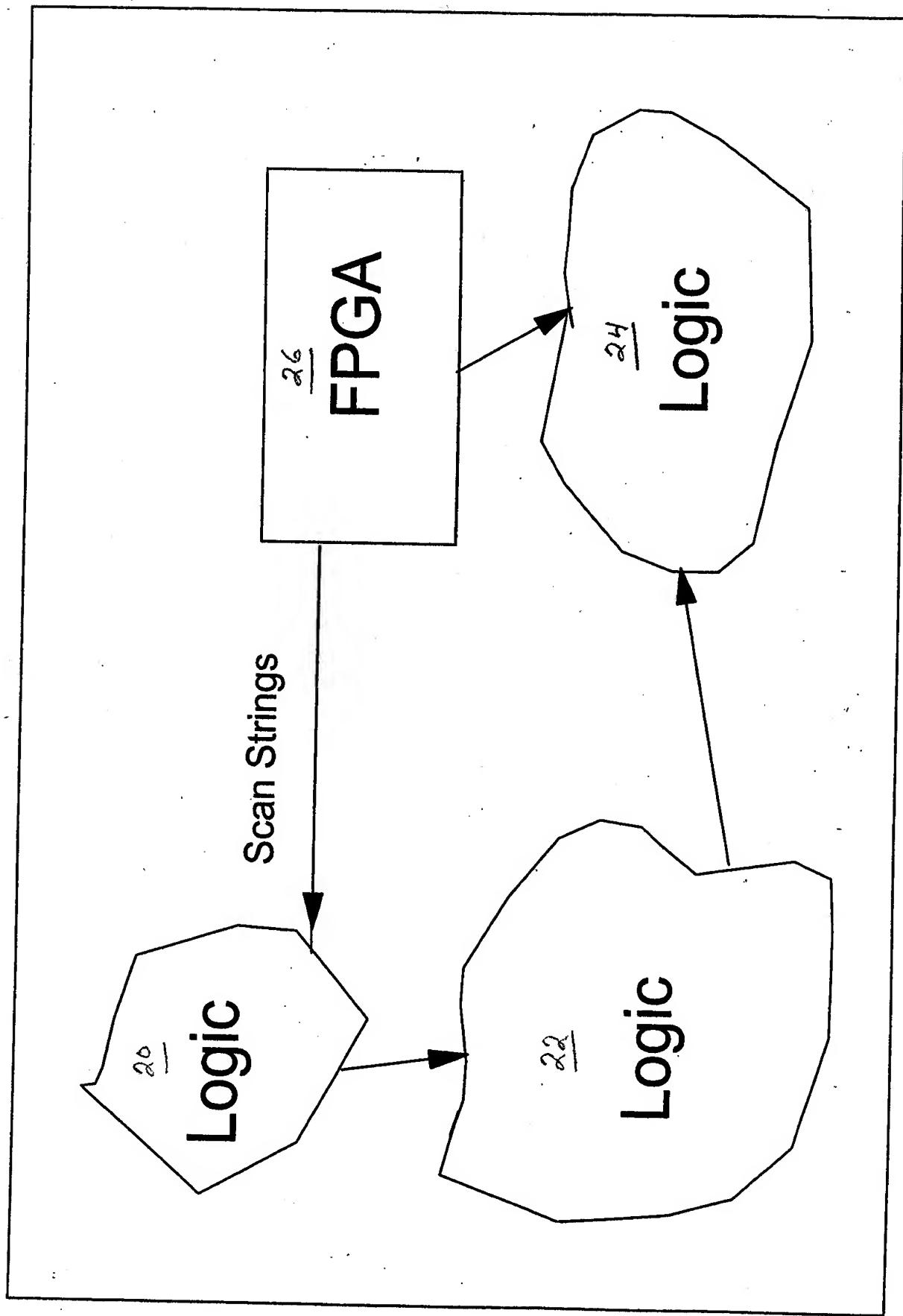
Logic Cones

Fig. 1



Structure

Fig. 2



Function to FPGA

Fig. 3

BUR920040001US1

3/3

